Abstracts

A Three-Bit Monolithic Phase Shifter at V-Band

A.W. Jacomb-Hood, D. Seielstad and J.D. Merrill. "A Three-Bit Monolithic Phase Shifter at V-Band." 1987 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 87.1 (1987 [MCS]): 81-84.

This paper describes the design and performance of a three-bit monolithic phase shifter at V-band. The selected circuit approach was a reflection phase shifter with switched delay lines. Schottky diodes were used as the switching elements. Tested at 62.5 GHz, the RMS phase error was 2.7°, the insertion loss 10.8 ± 1.8 dB (including fixture loss), and the VSWR was better than 2.1:1. The maximum DC power requirement was 40 mW.

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